	Search Text	DBs
1	(simultaneous$4 near3 broadcast$4) with ((multiple or plural$3) adj processors)	USPAT; EPO; JPO
2	(simultaneous$4 near3 load$4) with ((multiple or plural$3) adj processors)	USPAT; EPO; JPO
3	(simultaneous$4 with ((multiple or plural$3) adj processors)) same (initializ$5 or configur$5)	USPAT; EPO; JPO
4	(simultaneous$4 with ((multiple or plural$3) adj processors)) with (initializ$5)	USPAT; EPO; JPO
5	(simultaneous$4 with ((multiple or plural$3) adj processors)) with (configuat$5)	USPAT; EPO; JPO
6	(simultaneous$4 with ((multiple or plural$3) adj processors)) with (configur$5)	USPAT; EPO; JPO
7	((simultaneous$4 with ((multiple or plural$3) adj processors)) with (configur$5)) and (control adj register)	USPAT; EPO; JPO

	Search Text	DBs
8	("5430861" "5274782" "4796260" "4855903" "5524235" "5797027" "5930518" "6038391" "4313160" "4402045" "4847842" "4924426" "5524234" "5553094" "5561784" "5664149" "5673252" "5860111" "5963146" "5968160" "6172616" "6233668" "4424575" "4430710" "4569048" "4602331" "4849974" "4849976" "5197130" "5222224" "5237670" "5280584"	USPAT; EPO; JPO
9	711/100.ccls.	USPAT; EPO; JPO
10	711/119.ccls.	USPAT; EPO; JPO
11	711/154.ccls.	USPAT; EPO; JPO
12	711/156.ccls.	USPAT; EPO; JPO
13	709/200.ccls.	USPAT; EPO; JPO
14	711/200.ccls.	USPAT; EPO; JPO
15	711/144.ccls.	USPAT; EPO; JPO
16	712/1.ccls.	USPAT; EPO; JPO
17	712/10.ccls.	USPAT; EPO; JPO
18	711/100.ccls. and 711/144.ccls.	USPAT; EPO; JPO

	Search Text	DBs
19	711/100.ccls. and 711/154.ccls.	USPAT; EPO; JPO
20	712/1.ccls. and 711/154.ccls.	USPAT; EPO; JPO
21	712/10.ccls. and 711/154.ccls.	USPAT; EPO; JPO
22	710/36.ccls.	USPAT; EPO; JPO
23	710/36.ccls. and 712/1.ccls.	USPAT; EPO; JPO
24	control adj register	USPAT; EPO; JPO
25	(control adj register) and (data adj control adj register)	USPAT; EPO; JPO
26	(control adj register) same (data adj control adj register)	USPAT; EPO; JPO
27	(control adj register) same (data adj control adj register) same memory	USPAT; EPO; JPO
28	((control adj register) same (data adj control adj register) same memory) and ((simultaneous$4 with ((multiple or plural$3) adj processors)) same (initializ$5 or configur$5))	USPAT; EPO; JPO
29	((control adj register) same (data adj control adj register) same memory) and 712/1.ccls.	USPAT; EPO; JPO
30	((control adj register) same (data adj control adj register) same memory) and 712/10.ccls.	USPAT; EPO; JPO
31	(control adj register) same ((data adj control adj register) with memory)	USPAT; EPO; JPO
32	((control adj register) with memory) same ((data adj control adj register) with memory) same copy	USPAT; EPO; JPO
33	((control adj register) with memory) same ((data adj control adj register) with memory) same ((plurality or multi$4) adj memory)	USPAT; EPO; JPO
34	((control adj register) with memory) same ((data adj control adj register) with memory) same ((plurality or multi$4) adj subsystem)	USPAT; EPO; JPO
35	((control adj register) with memory) same ((data adj control adj register) with memory) and ((plurality or multi$4) adj subsystem)	USPAT; EPO; JPO
36	((control adj register)) same ((data adj control adj register) with memory) and ((plurality or multi$4) adj subsystem)	USPAT; EPO; JPO
37	((control adj register)) same ((data adj control adj register)) and ((plurality or multi$4) adj subsystem)	USPAT; EPO; JPO
38	((control adj register) with memory) same ((data adj control adj register) with memory)	USPAT; EPO; JPO